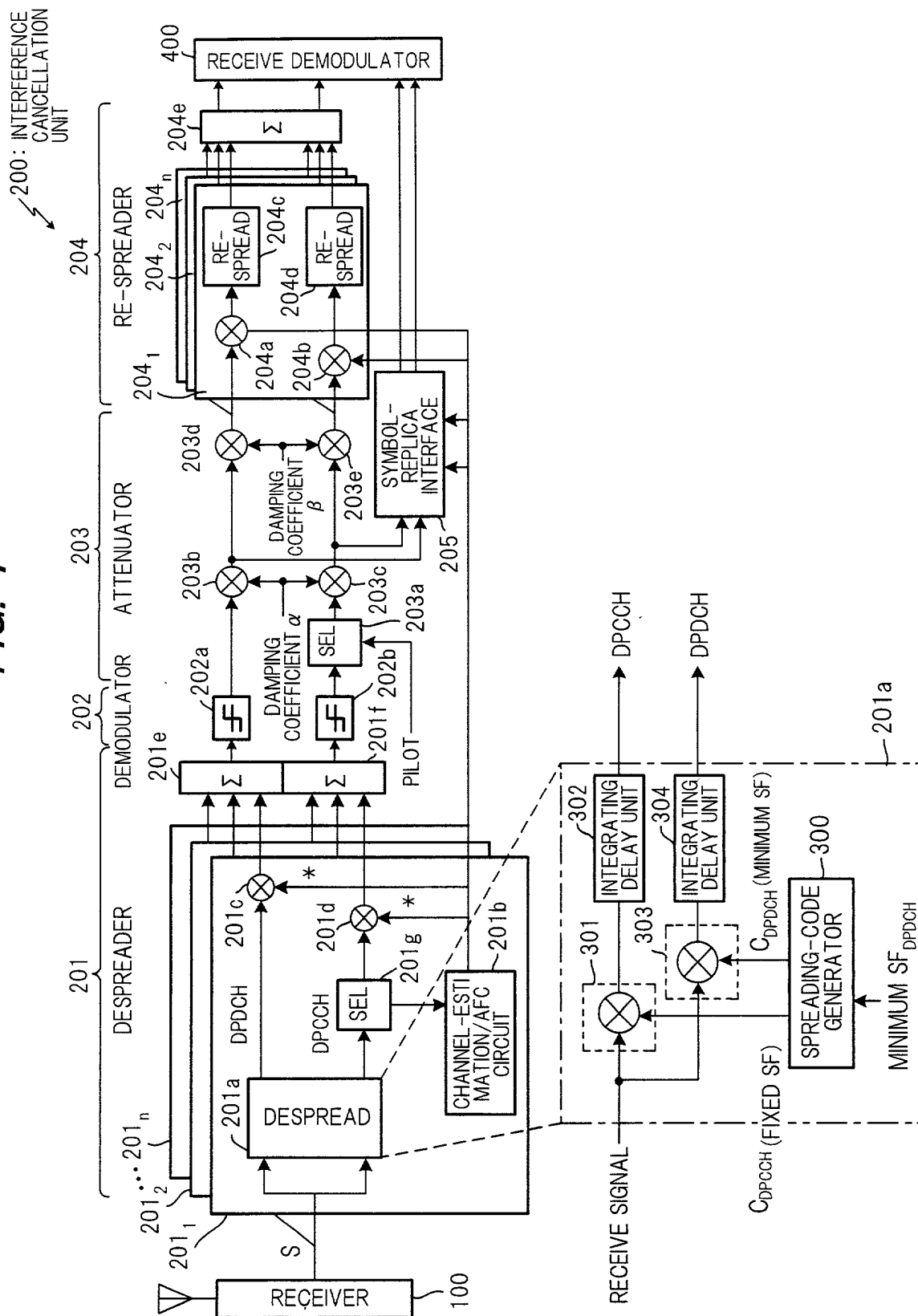


**FIG. 1**



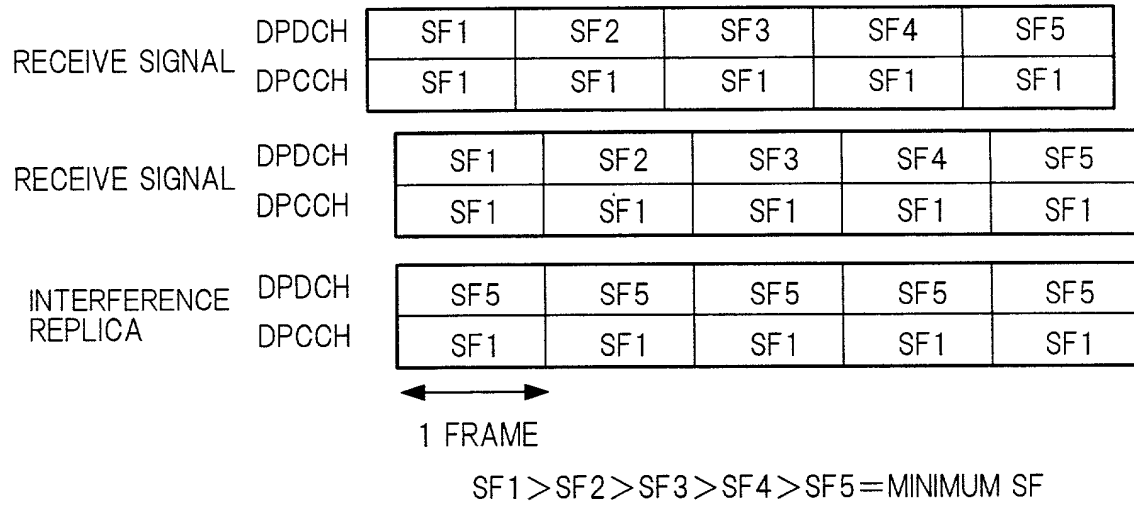
**FIG. 2**

FIG. 3

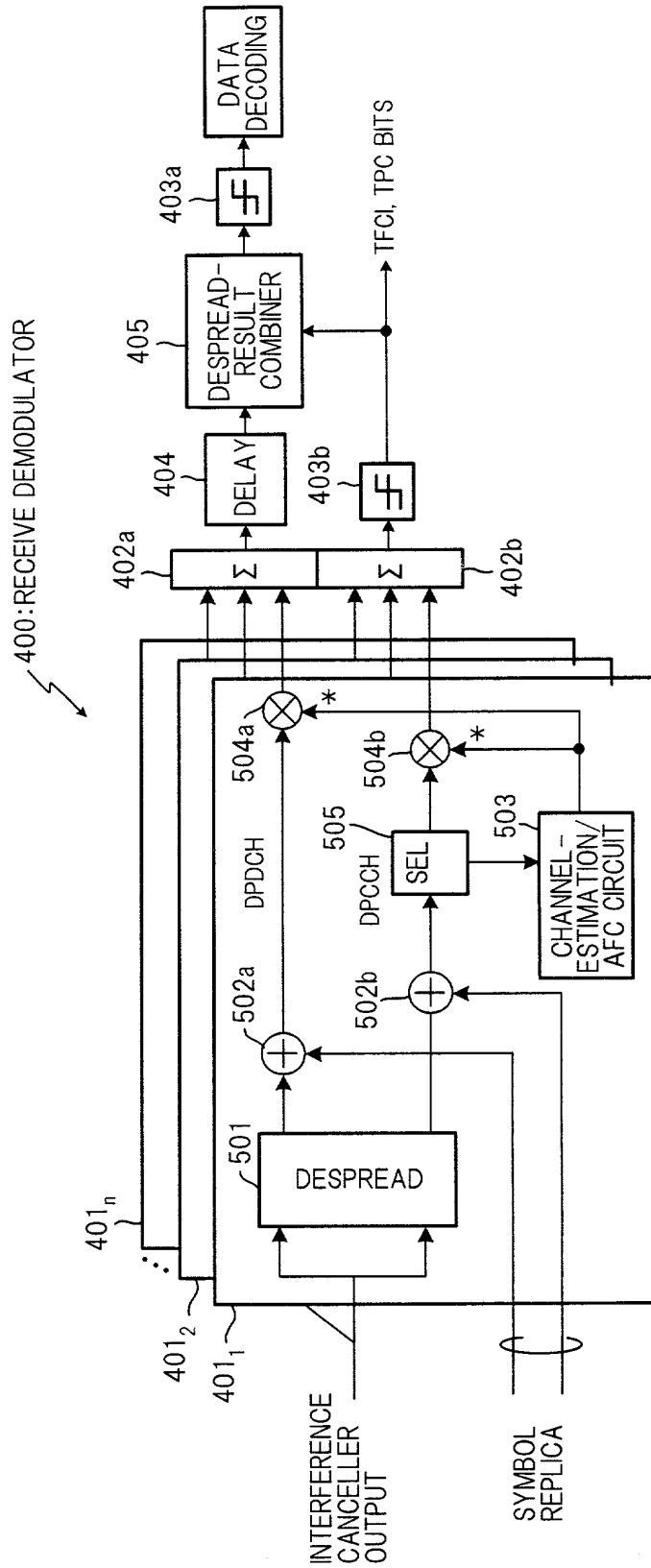
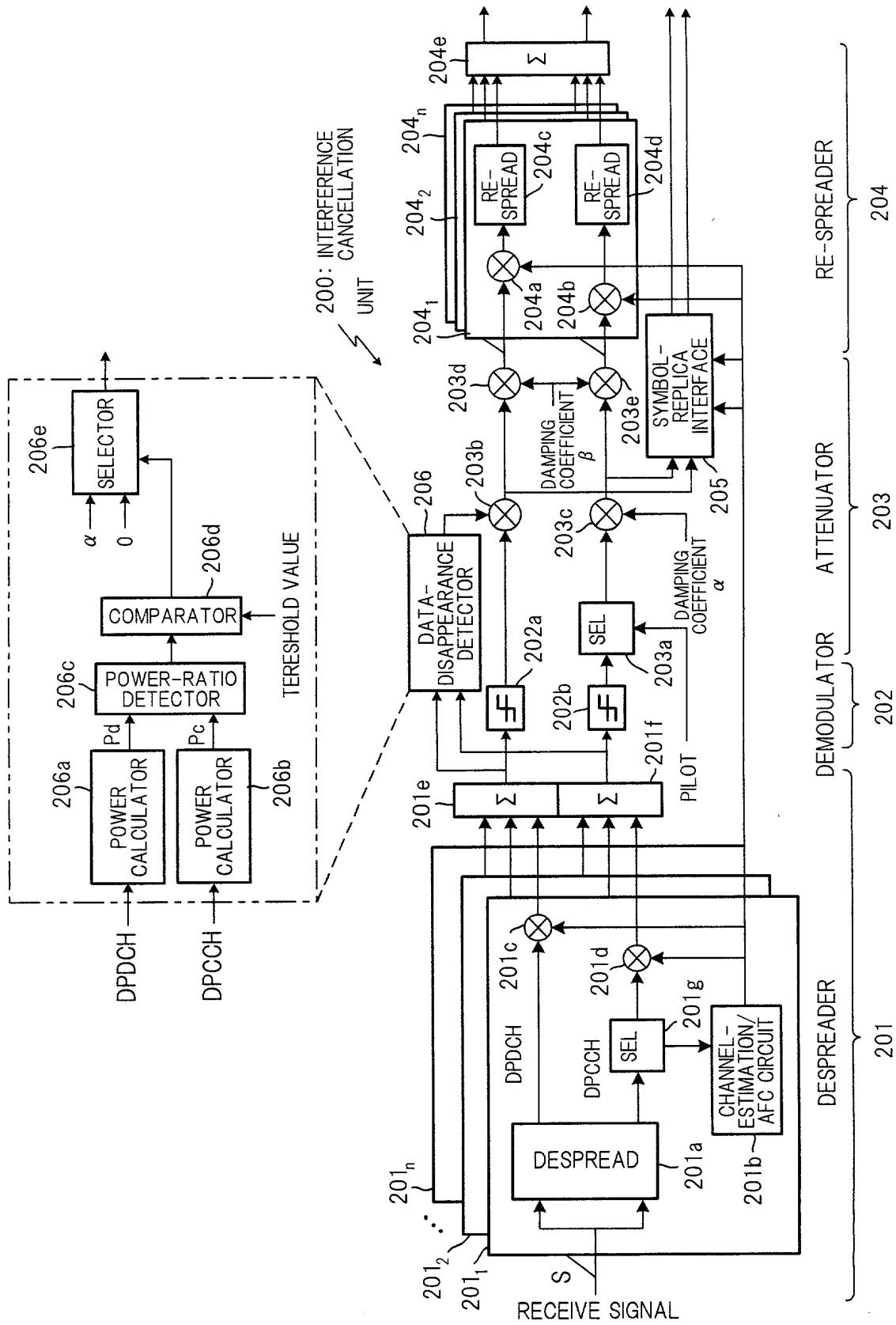


FIG. 4



The diagram illustrates a receiver system (200) designed for interference cancellation. The system is divided into four main functional blocks: DESPREAD (201), DEMODULATOR (202), ATTENUATOR (203), and RE-SPREADER (204).

**DESPREAD (201):** Receives a "RECEIVE SIGNAL" (S). It contains a "DESPREAD" block (201a) and a "CHANNEL-ESTIMATION/AFC CIRCUIT" (201b). The output of 201a is a set of signals  $201_1, 201_2, \dots, 201_n$ . The output of 201b is a "PILOT" signal (201f).

**DEMODULATOR (202):** Each signal  $201_i$  is multiplied by the pilot signal (201f) in a multiplier (201c). The results are summed in a summation block ( $\Sigma$ , 201e). The output of 201e is then filtered by a low-pass filter (202b).

**ATTENUATOR (203):** The filtered signals are selected by a selector (202a) and then multiplied by a damping coefficient  $\alpha$  in a multiplier (203a). The output of 203a is then filtered by another low-pass filter (203b).

**RE-SPREADER (204):** The signals from 203b are multiplied by a damping coefficient  $\beta$  in a multiplier (203c). The output of 203c is then spread by a "RE-SPREAD" block (204a). The spread signals are summed in a summation block ( $\Sigma$ , 204e).

**Detailed View of Damping Coefficient Decision Unit (207):** This unit is shown in a dashed box, receiving "DPDCH" and "DPCCH" signals. It contains two "POWER CALCULATOR" blocks (207a, 207b) and a "DISCRIMINATOR" block (207d). The outputs of 207a and 207b are compared in 207d to produce a damping coefficient  $\beta_c$ . This coefficient is then multiplied by a signal  $\alpha'$  in a multiplier (207e) to produce the final damping coefficient  $\alpha$ .

**Interference Cancellation Unit (200):** The entire system is labeled as the "INTERFERENCE CANCELLATION UNIT".

FIG. 6

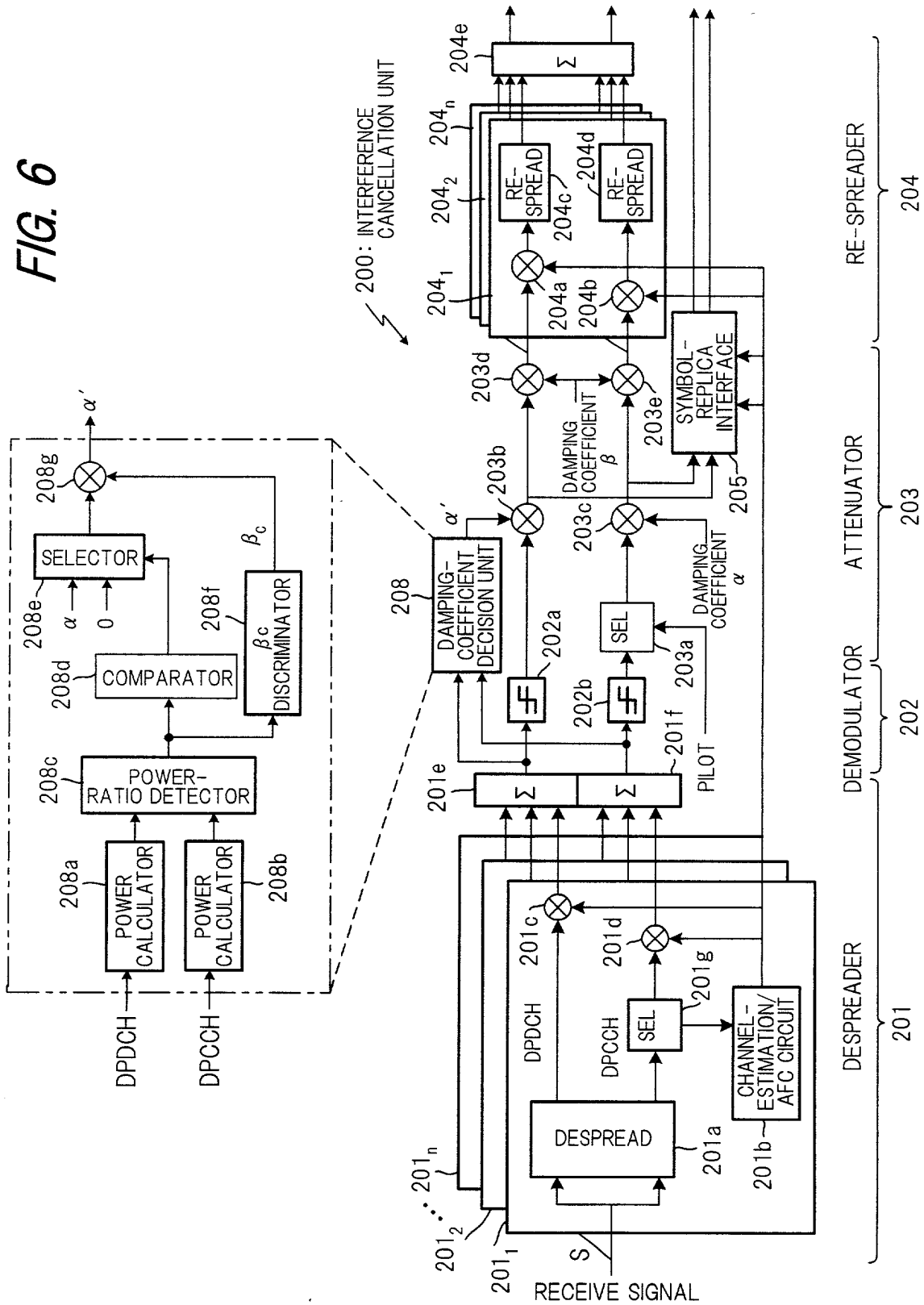


FIG. 7

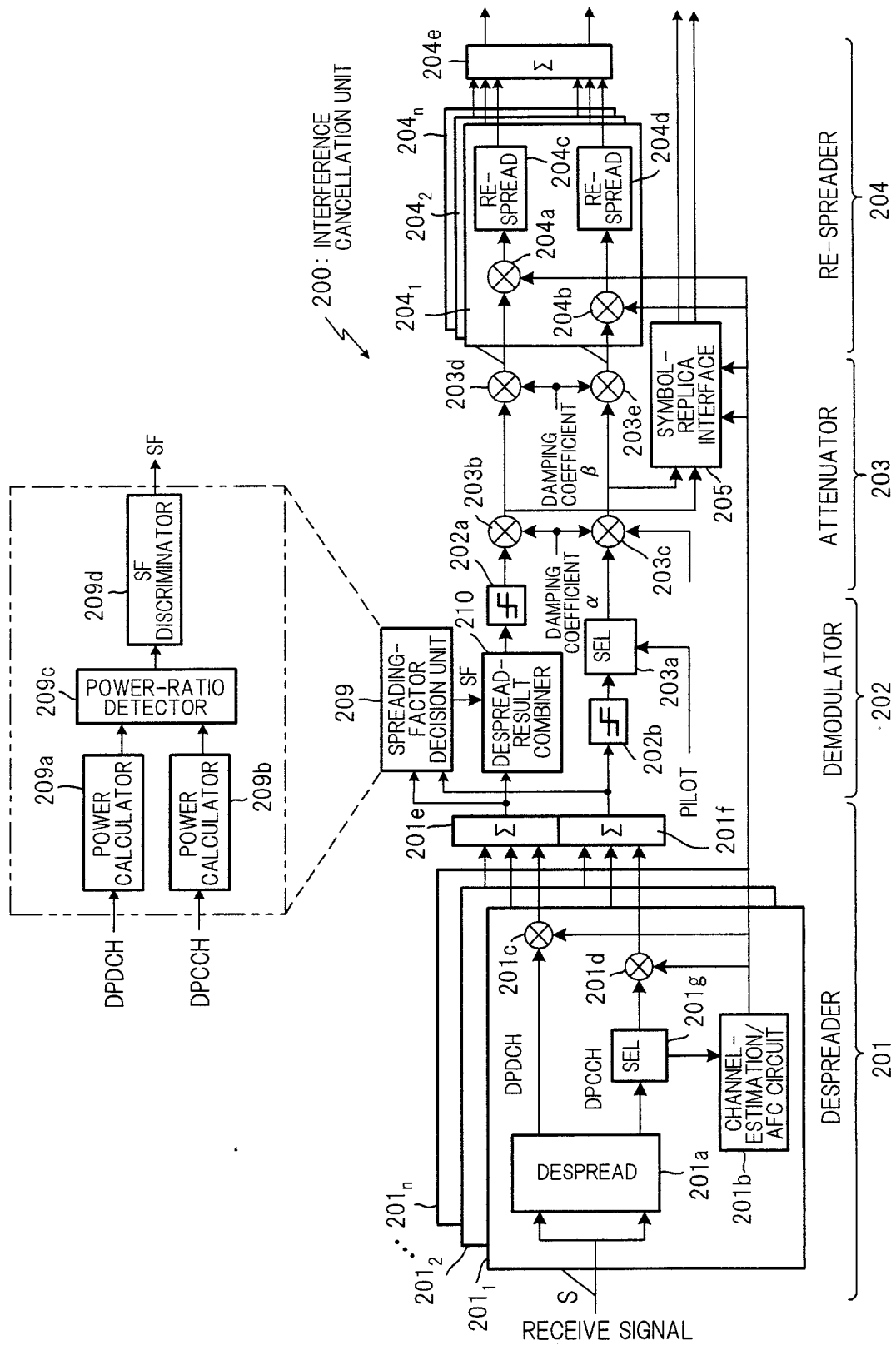


FIG. 8

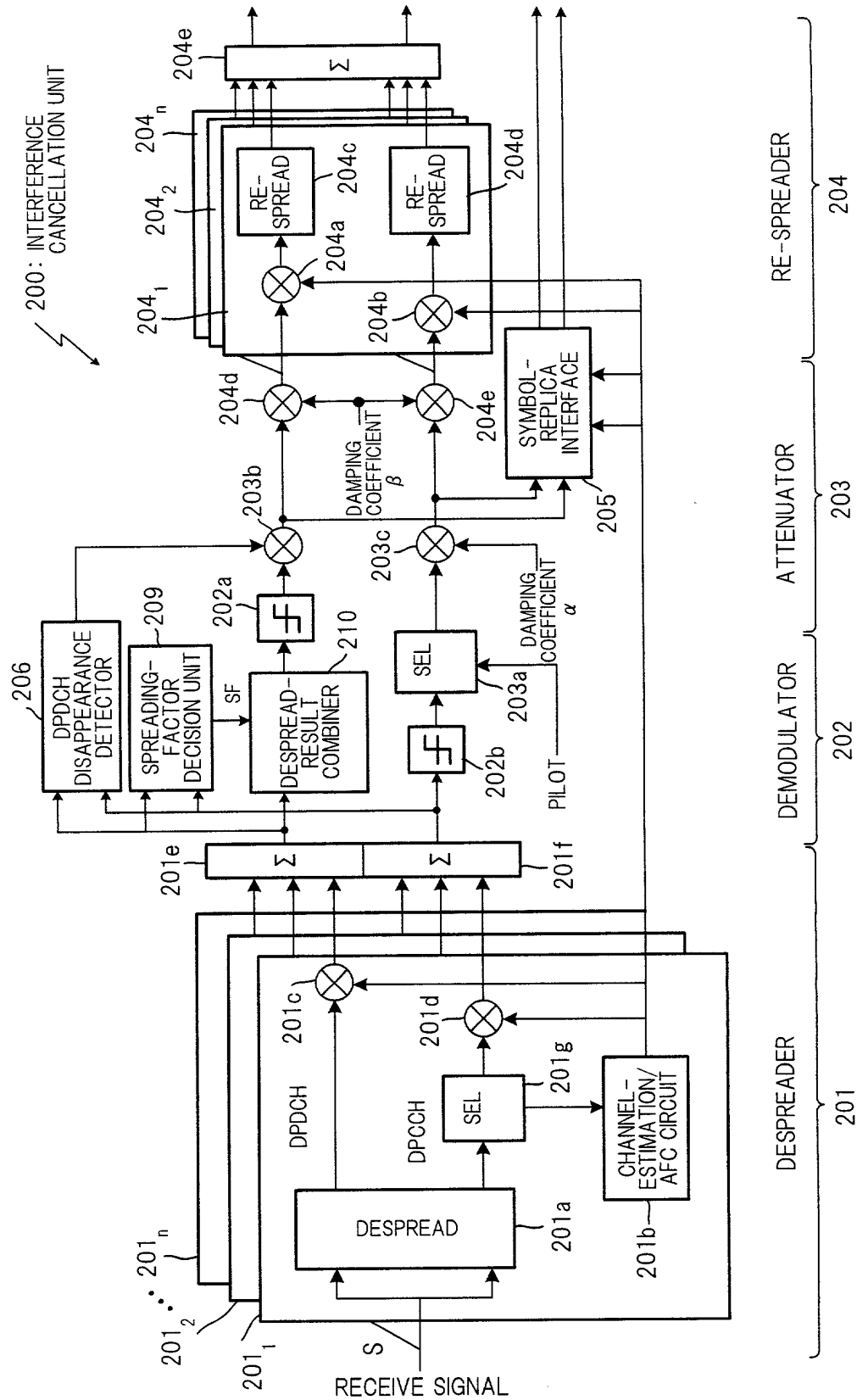




FIG. 9

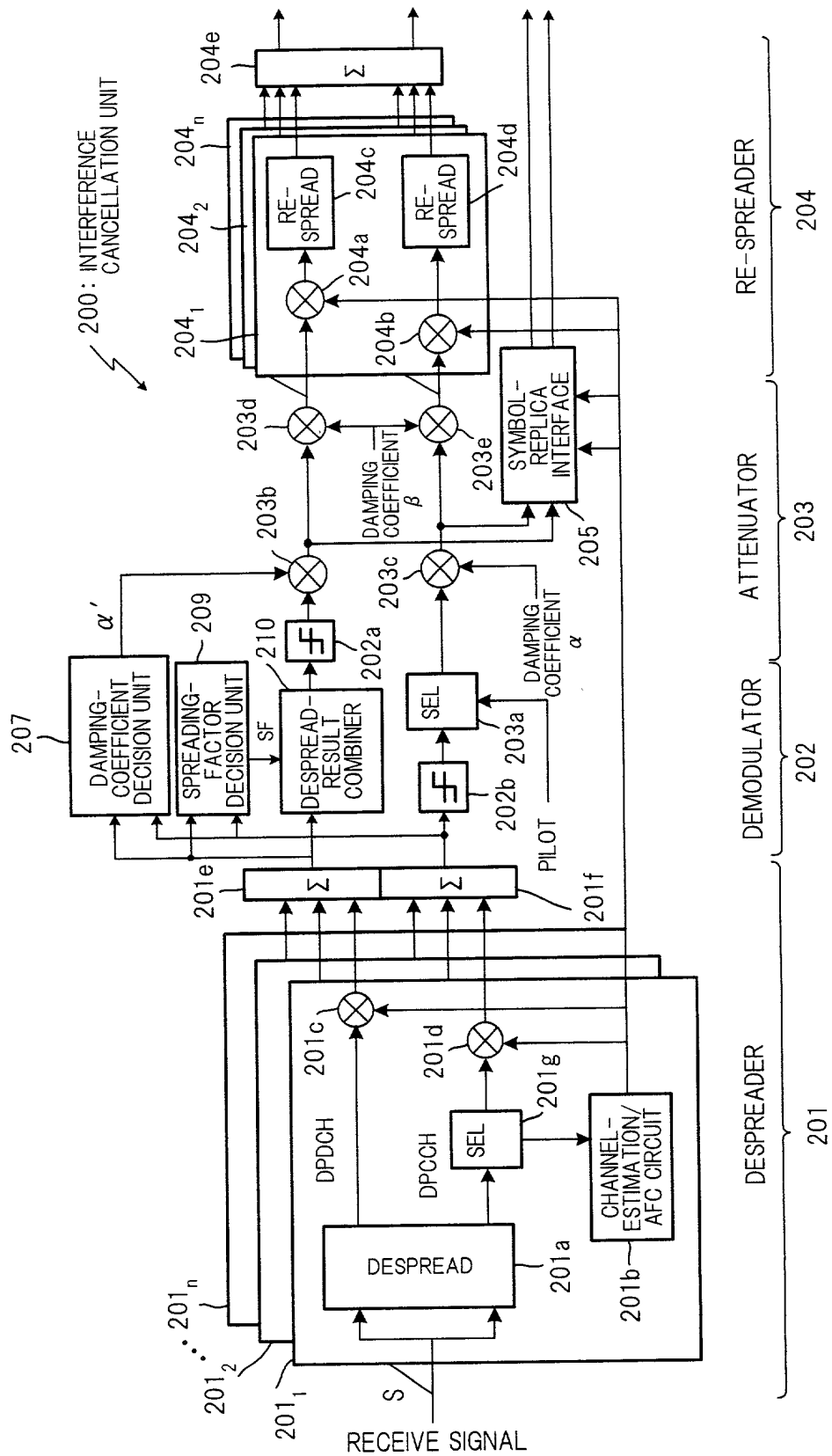
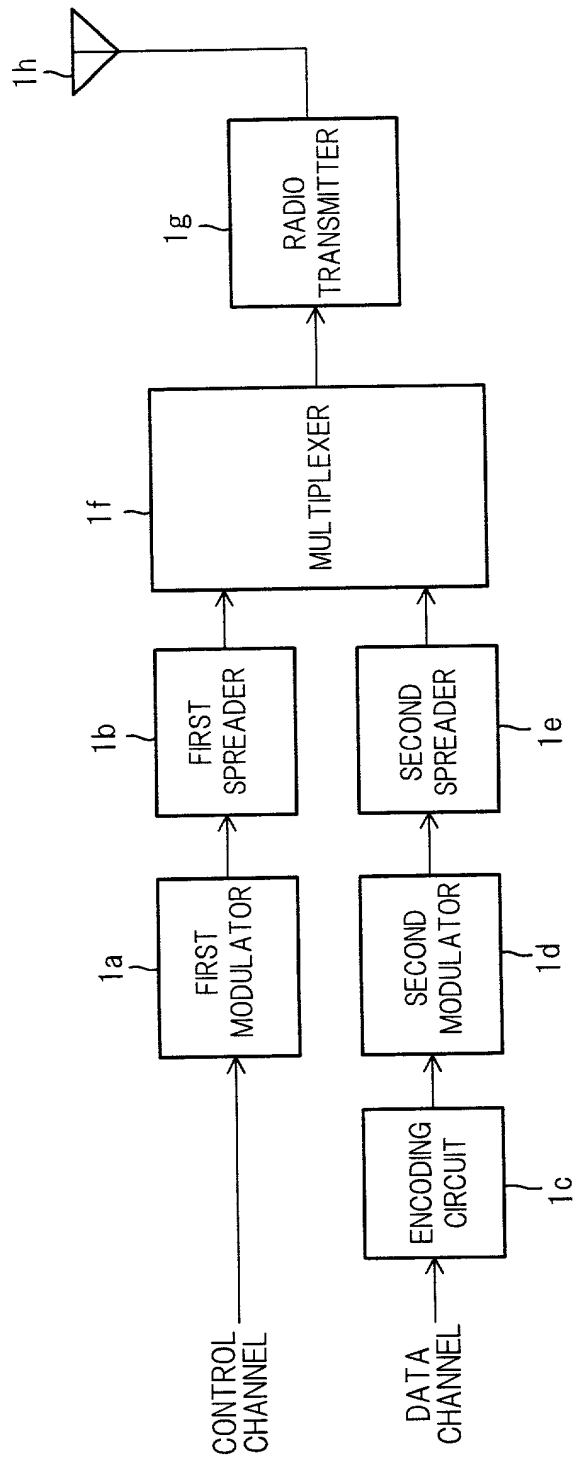


FIG. 10 PRIOR ART



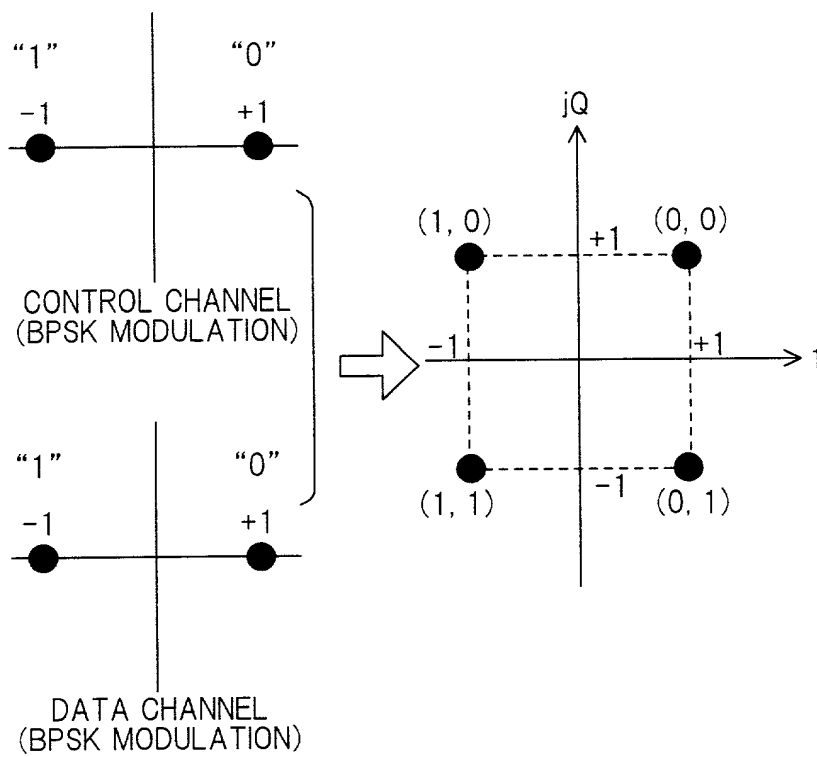
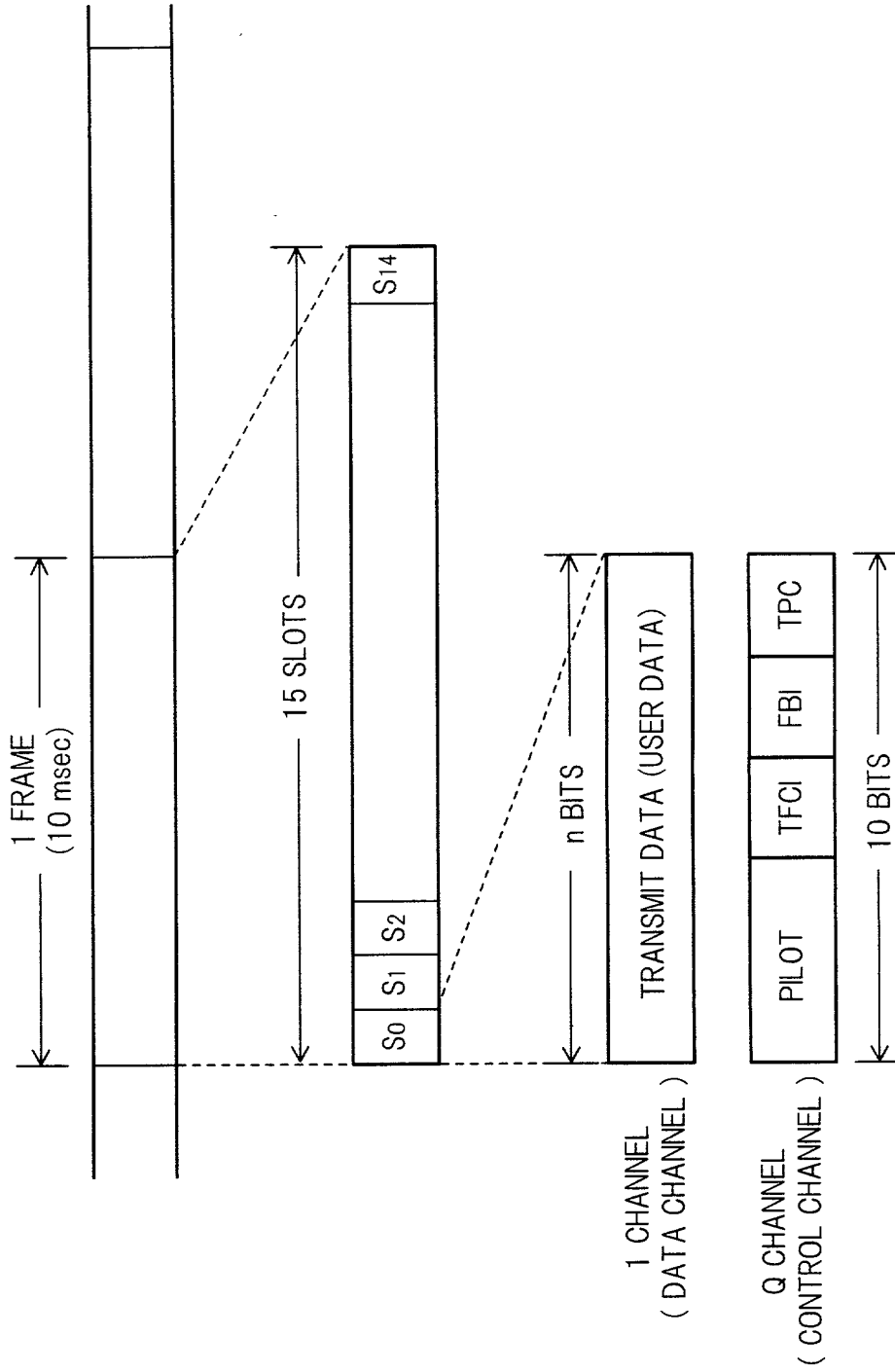
*FIG. 11 PRIOR ART*

FIG. 12 PRIOR ART



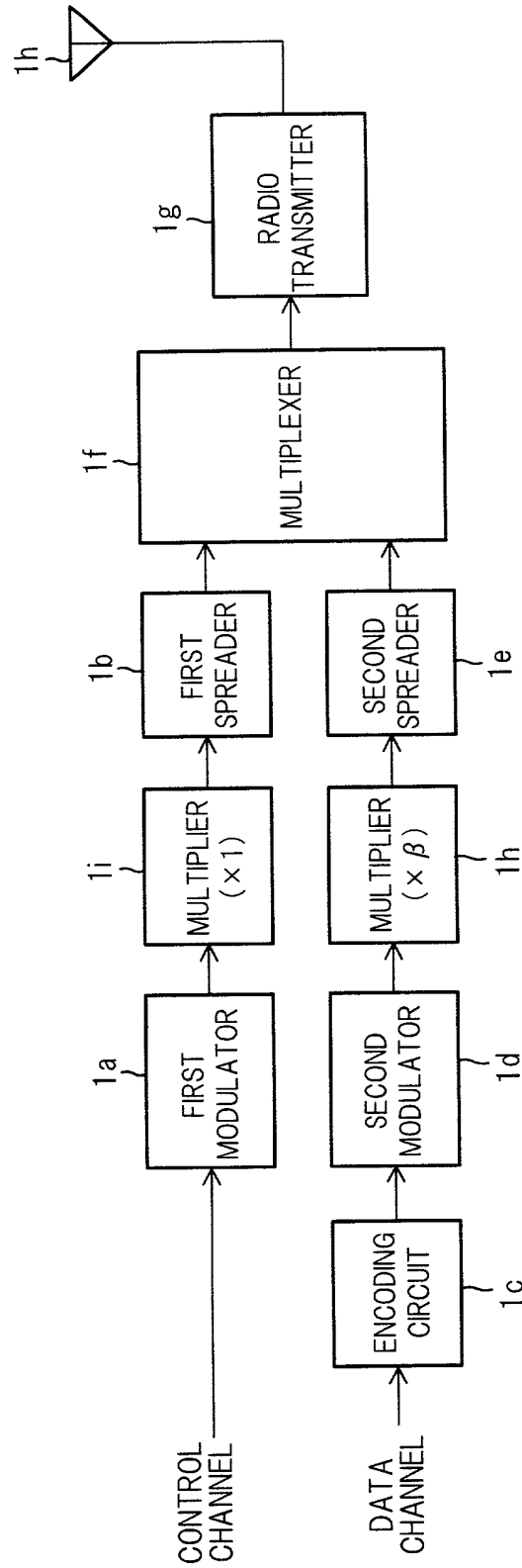


FIG. 13 PRIOR ART

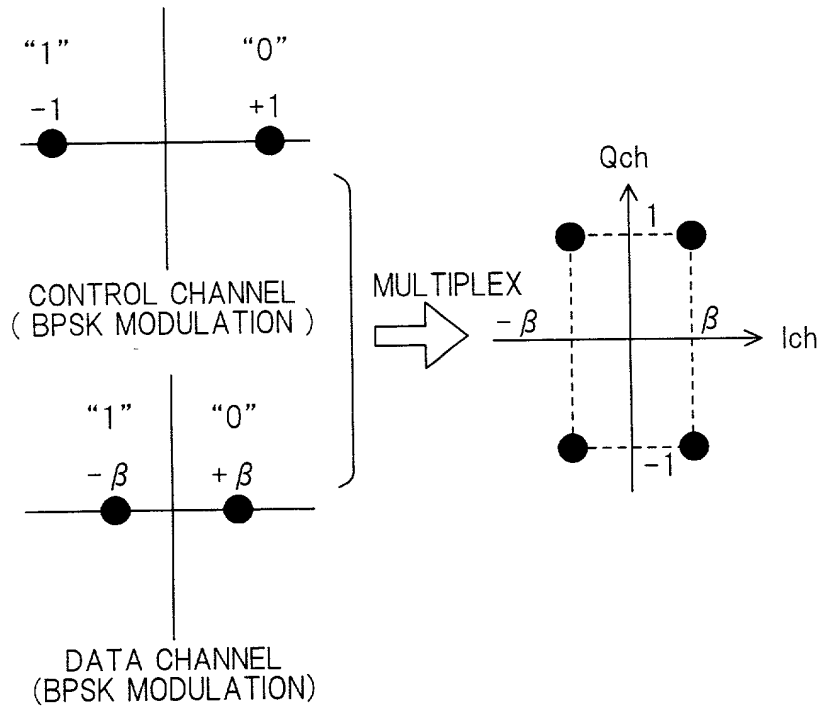
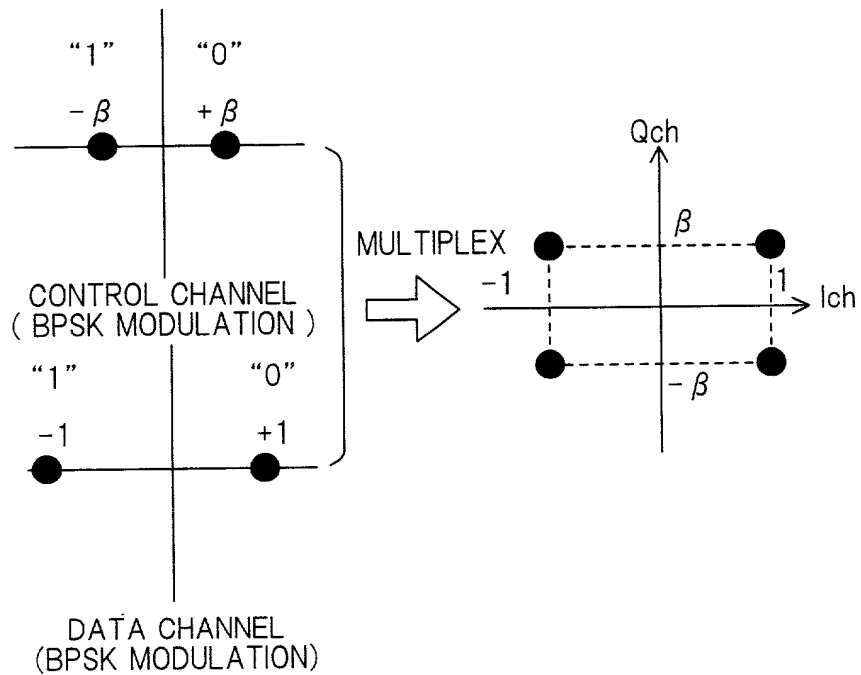
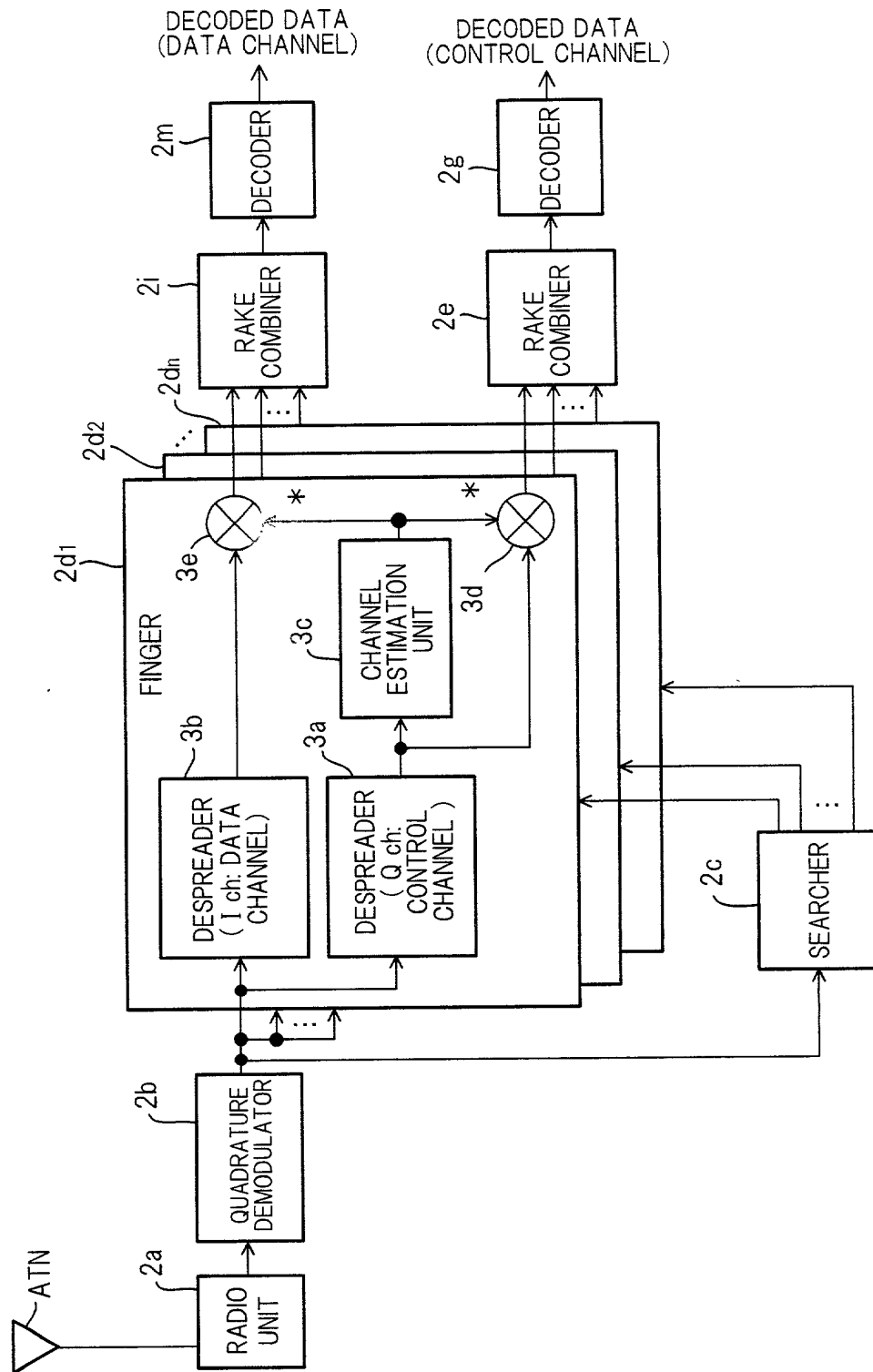
**FIG. 14A PRIOR ART****FIG. 14B PRIOR ART**

FIG. 15 PRIOR ART



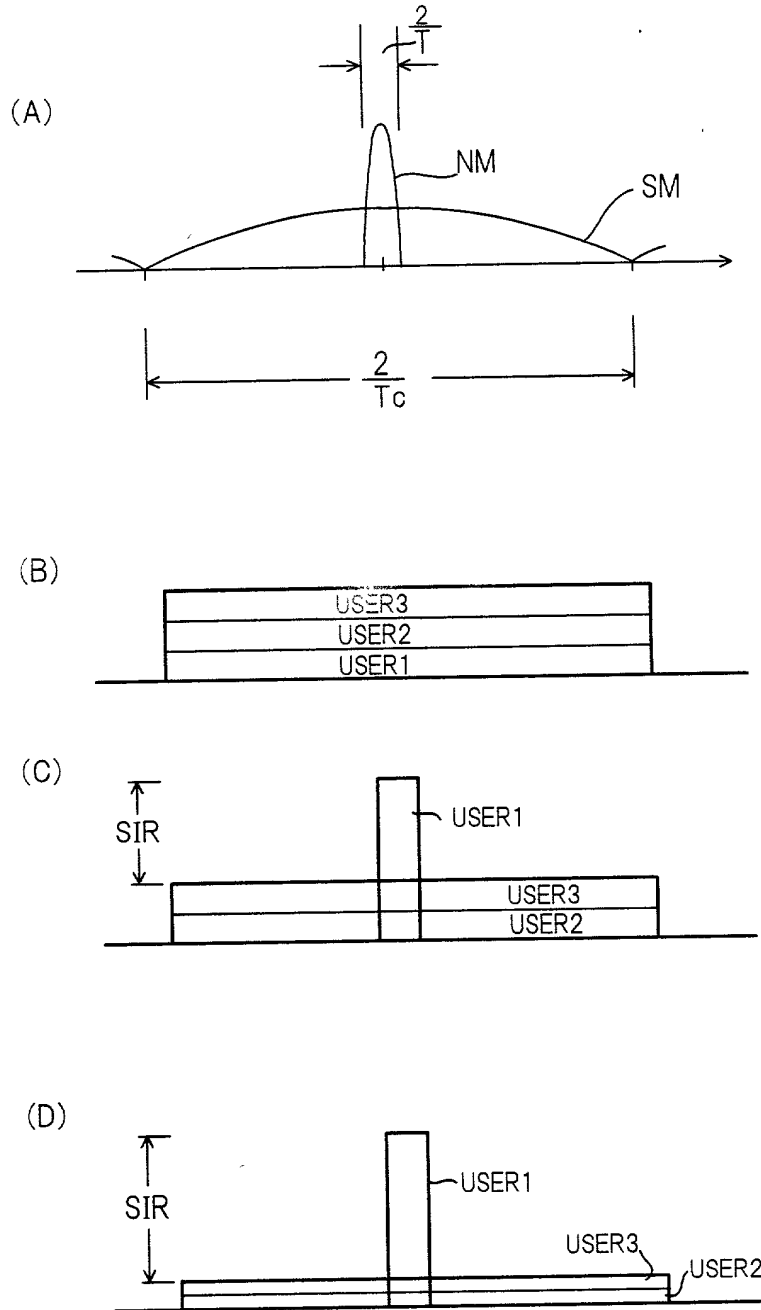
**FIG. 16 PRIOR ART**



FIG. 17 PRIOR ART

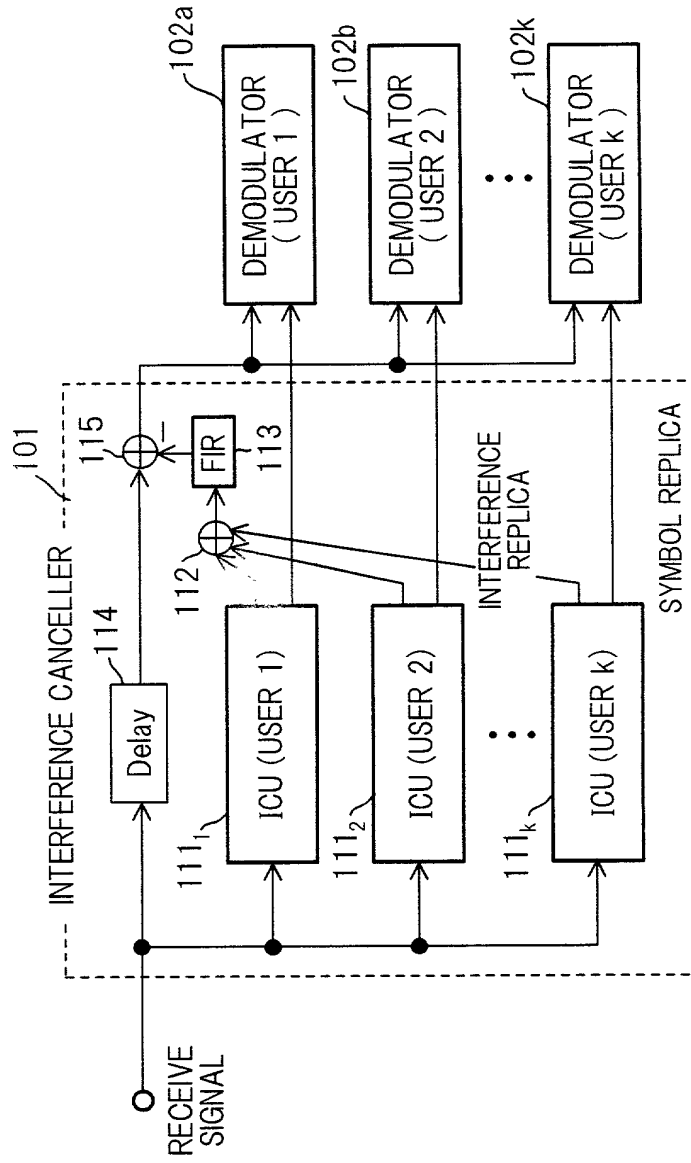
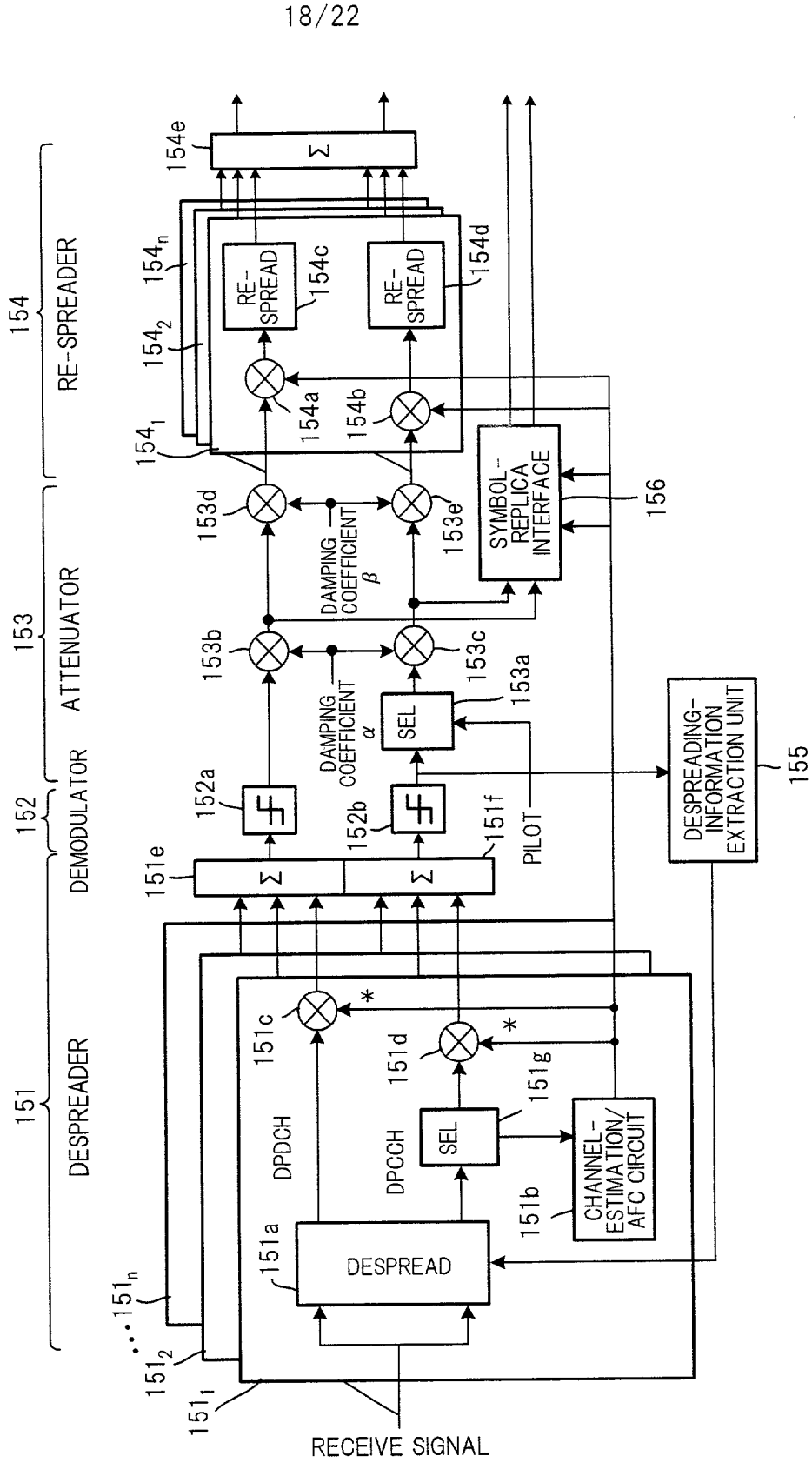
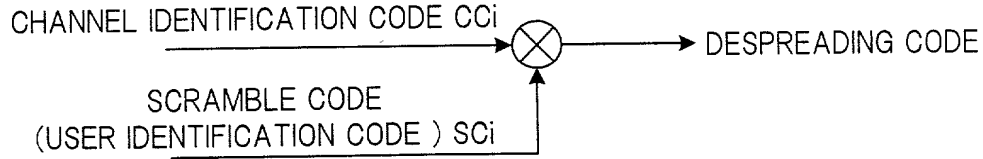
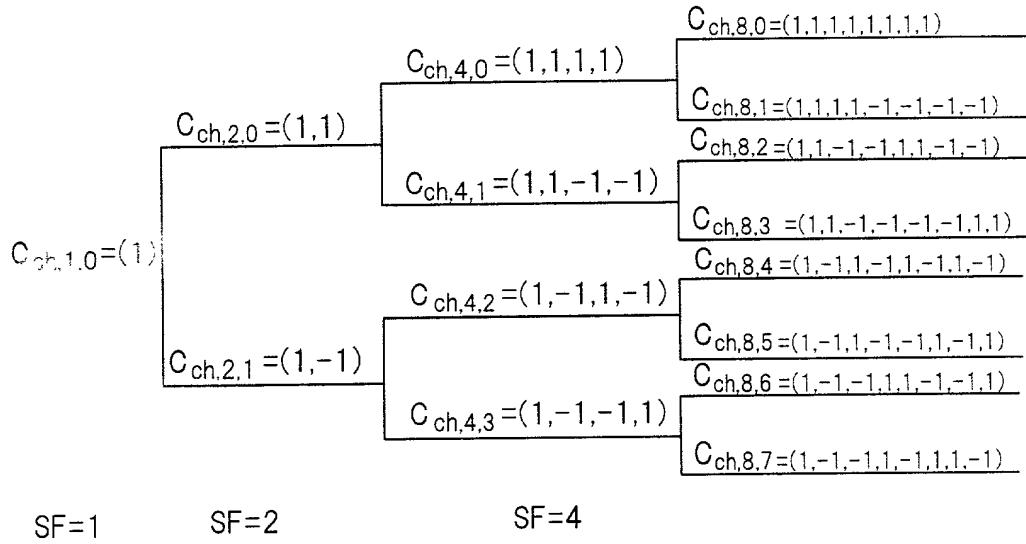


FIG. 18 PRIOR ART



**FIG. 19 PRIOR ART****FIG. 20A PRIOR ART****FIG. 20B PRIOR ART**

$$C_{ch,1,0} = 1$$

$$\begin{bmatrix} C_{ch,2,0} \\ C_{ch,2,1} \end{bmatrix} = \begin{bmatrix} C_{ch,1,0} & C_{ch,1,0} \\ C_{ch,1,0} & -C_{ch,1,0} \end{bmatrix} = \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix}$$

$$\begin{bmatrix} C_{ch,2(n+1),0} \\ C_{ch,2(n+1),1} \\ C_{ch,2(n+1),2} \\ C_{ch,2(n+1),3} \\ \vdots \\ C_{ch,2(n+1),2(n+1)-2} \\ C_{ch,2(n+1),2(n+1)-1} \end{bmatrix} = \begin{bmatrix} C_{ch,2^n,0} & C_{ch,2^n,0} \\ C_{ch,2^n,0} & -C_{ch,2^n,0} \\ C_{ch,2^n,1} & C_{ch,2^n,1} \\ C_{ch,2^n,1} & -C_{ch,2^n,1} \\ \vdots & \vdots \\ C_{ch,2^n,2^n-1} & C_{ch,2^n,2^n-1} \\ C_{ch,2^n,2^n-1} & -C_{ch,2^n,2^n-1} \end{bmatrix}$$

**FIG. 21 PRIOR ART**

FOR SPREADING FACTOR SF (=16)

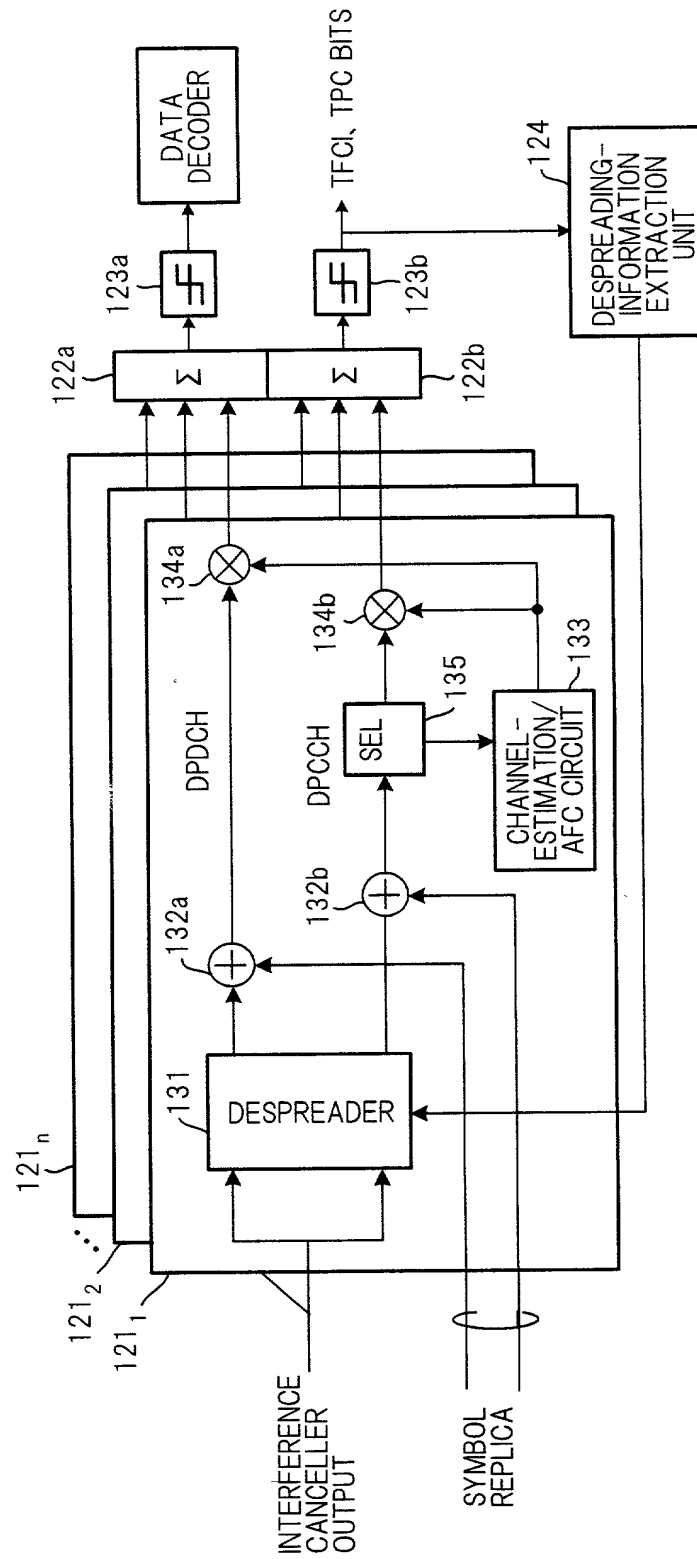
SYMBOL DATA	Data1	Data2
CHANNEL IDENTIFICATION CODE	"0011001100110011"	"0011001100110011"

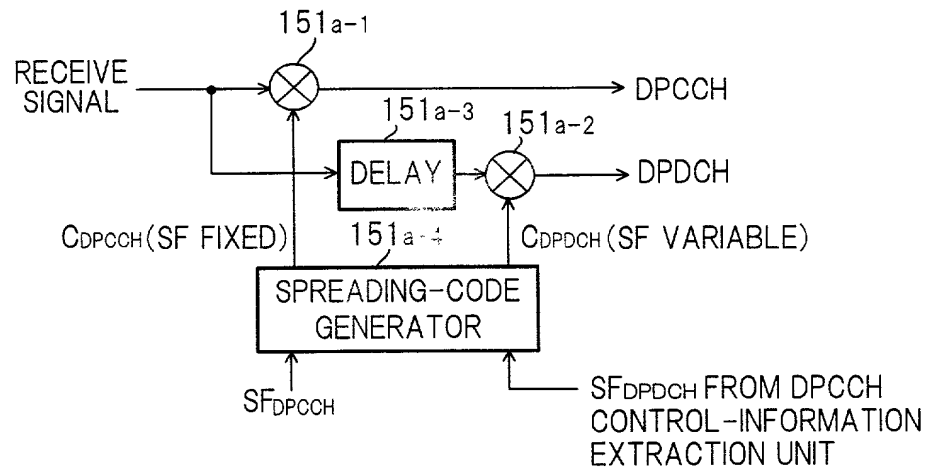
FOR SPREADING FACTOR  $SF_{\min} (=4)$ 

Symbol Data	Channel Identification Code	Data1	Data2	Data3	Data4	Data5	Data6	Data7	Data8
		"0011"	"0011"	"0011"	"0011"	"0011"	"0011"	"0011"	"0011"

SF/SF<sub>min</sub>

FIG. 22 PRIOR ART



**FIG. 23 PRIOR ART****FIG. 24 PRIOR ART**

RECEIVE SIGNAL	DPDCH	SF1	SF2	SF3	SF4	SF5
	DPCCH	SF1	SF1	SF1	SF1	SF1
DELAYED RECEIVE SIGNAL	DPDCH	SF1	SF2	SF3	SF4	
	DPCCH	SF1	SF1	SF1	SF1	
INTERFERENCE REPLICA	DPDCH	SF1	SF2	SF3	SF4	
	DPCCH	SF1	SF1	SF1	SF1	

$\longleftrightarrow$   
 1 FRAME